Frank O'Mahony et al. Applicant: FOR USE IN **MICROELECTRONIC** Title: LOW LOSS INTERGO URE **CIRCUITS** Serial No.: 09/893023 Docket No.: 884.405US1 Due Date: October 16, 2003 June 27, 2001 Filed: Examiner: Ahmed Sefer Group Art Unit: 2826 Confirmation No.: 3406 Customer No.: 21186 MS AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 We are transmitting herewith the following attached items (as indicated with an "X"): A return postcard. $\underline{\mathbf{X}}$ $\overline{\underline{\mathbf{X}}}$ An Amendment and Response Under 37 CFR 1.116 (10 Pages). Communication Concerning Related Patents (1 pg.). If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743. SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Customer Number 21186

JE, 2200

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this \mathcal{L} day of August, 2003.

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Name

Customer Number 21186

Kacia Zii

(GENERAL)

9/893023

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Frank O'Mahony et al.

Examiner: Ahmed Sefer

Serial No.:

09/893023

Group Art Unit: 2826

Filed:

June 27, 2001

Docket: 884.405US1

Title:

LOW LOSS INTERCONNECT STRUCTURE FOR USE IN

MICROELECTRONIC CIRCUITS

Assignee:

Intel Corporation

Customer No.: 21186

COMMUNICATION CONCERNING RELATED PATENTS

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related patents in the above-identified patent application:

Patent No.

Serial No.

Filing Date

Attorney Docket

6,522,186

09/893,067

June 27, 2001

884.431US1

Respectfully submitted,

FRANK O'MAHONY ET AL.

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Alexandria, VA 22313-1450, on this 29 day of August, 2003.

Signature Lilia Lil

EXPEDITED PROCEDURE – EXAMINING GROUP 2826

PATENT S/N 09/893023

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Frank O'Mahony et al.

Examiner: Ahmed Sefer

Serial No.:

09/893023

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OIP & Title:

June 27, 2001

LOW LOSS INTERCONNECT STRUCTURE FOR USE IN

MICROELECTRONIC CIRCUITS

SEP 0 2 2003 ssignee:

Intel Corporation

Customer No.: 21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

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In response to the final Office Action mailed July 16, 2003, please amend the application as follows: